

SPECIFICATION FOR APPROVAL

Product	Type:	Graphic	Type	STN	Dot	Matrix
		LCD Mo	<u>odule</u>			

Part No.: SD-G12232A-YFDN-DYWC

Customer:	
Customer Part No.:_	
Date:	

APPROVED SIGNATURES

SANTECH	Customer

SANTECH DISPLAY CO.,LTD



- 1. REVISION RECORD
- 2. GENERAL SPECIFICATION
- 3. OUTLINE DEMENSION:
- 4. BLOCK DIAGRAM
- 5. ABSOLUTE MAXIMUM RATINGS
- 6. ELECTRICAL CHARACTERISTICS
- 7. ABSOLUTE MAXIMUM RATINGS FOR LED BACKLIGHT
- 8. PIN ASSIGNMENT
- 9. MPU INTERFACE
- 10. REFLECTOR OF SCREEN AND DIPLAY RAM
- 11. DISPLAY CONTROL INSTRUCTION
- 12. OPTICAL CHARACTERISTICS
- 13. POWER SUPPLY SCHEMATICS
- 14. APPLICATION EXAMPLE
- 15. PRECAUTION FOR USING LCM



1. REVISION RECORD

REV	DATA	PAGES	DESCRIPTION
V1.0	20070320		NEW VER



2. GENERAL SPECIFICATION

122×32 dots display

LCD driver: 1520

Interface with 8-bit MPU (directly connected to M6800serial MPU)

Display specification

Display dot: 122×32

Display type: STN

Display color-Display background color: BLACK-YELLOW/GREEN

Polarizer mode: positive; Transflective

Viewing angle: 6:00

Display RAM: 5120 bits

Mechanical characteristics (Unit: mm)

External dimension: $84 \times 44 \times 13.5$

View area: 60.0X18.5

Dot size: 0.4X0.4

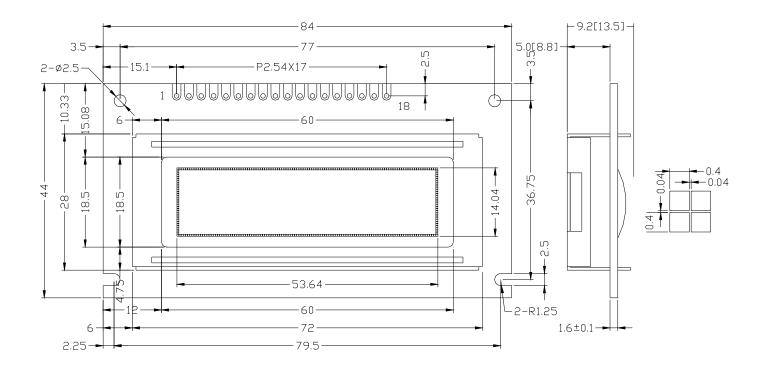
Dot pitch: 0.44X0.44

Weight: (g)

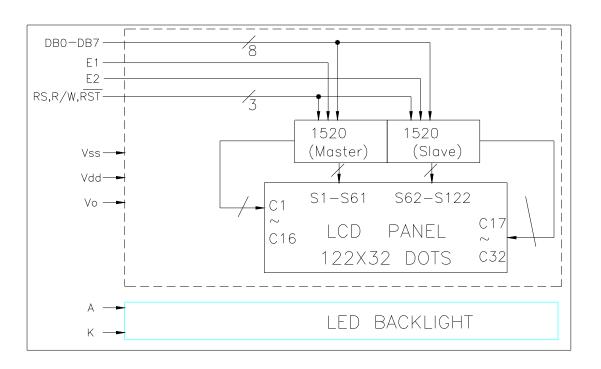
Power: +5V



3. OUTLINE DEMENSION:



4. BLOCK DIAGRAM:





5. Absolute Maximum Ratings

			Standar		
ltem	Symbol	Condition	Min	Max	Unit
Supply Voltage for logic	Vdd		-0.3	7.0	V
Supply Voltage for LCD	Vo	Ta=25 ℃	-0.3	Vdd+0.3	V
Input Voltage	Vi		-0.3	Vdd+0.3	V
Operating Temp(T)	Тор	-	-10	60	$^{\circ}$
Storage Temp (T)	Tstg	-	-20	70	$^{\circ}\mathbb{C}$

6. **ELECTRICAL SPECIFICATIONS**(Ta=25°C,Vdd=5.0V)

ltem	Symbol	Condition	Star	Unit		
item	Symbol Condition		Min	Type	Max	UIII
Supply Voltage for logic	Vdd-Vss	-	4.5	5.0	5.5	V
Supply Current for logic	Idd	Vdd=5.0	-	150	300	uA
Driving Current for LCD	Io	Vo=4.7	-	100	200	uA
Driving Voltage for LCD	Vdd-Vo	-	-	4.7	-	V
Input Voltage H level	V_{IH}	-	2.0	-	Vdd	V
Input Voltage L level	$V_{\rm IL}$	-	Vss	-	0.8	V
Output Voltage H level	V_{OH}	I _{OH} =-3.0m A	2.4	_	-	V
Output Voltage L level	V_{OL}	I _{OL} =3.0m A	-	-	0.4	V

7. Absolute Maximum Ratings For Bottom LED Backlight

Parameter	Symbol	Test condition	Min	Type	Max	Unit
LED Forward Consumption Current	$ m I_f$	Ta=25 ⁰ C	-	100	150	mA
LED Allowable Dissipation	P_d	Vf=4.2V	1	420	630	mW

8. Pin assignment

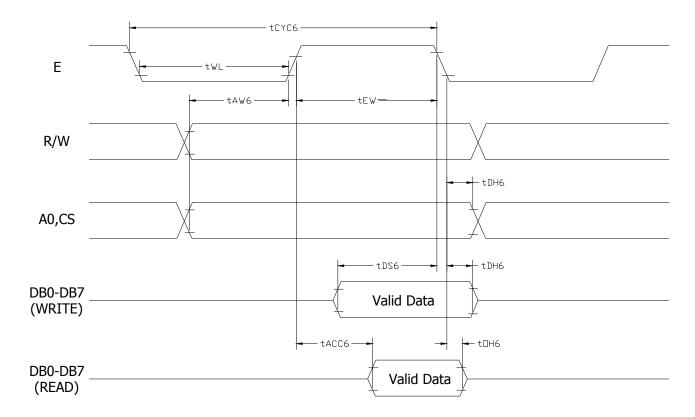
Pin NO	Symbol	Fund	Function					
1	Vss		0V					
2	Vdd	Power supply	+5V					
3	Vo		For LCD	Variable				
4	RS	Register select H: data L:	instruction					
5	E1	Read/Write Enable Signal(S	Read/Write Enable Signal(Slave)					
6	E2	Read/Write Enable Signal(I	Read/Write Enable Signal(Master)					
7	R/W	Read/write Select Signal						
8-15	DB0~DB7	Data bus						
16	/RES	System Reset						
17	A	Anode of LED Unit						
18	K	Cathode of LED Unit	Cathode of LED Unit					



9. MPU Interface

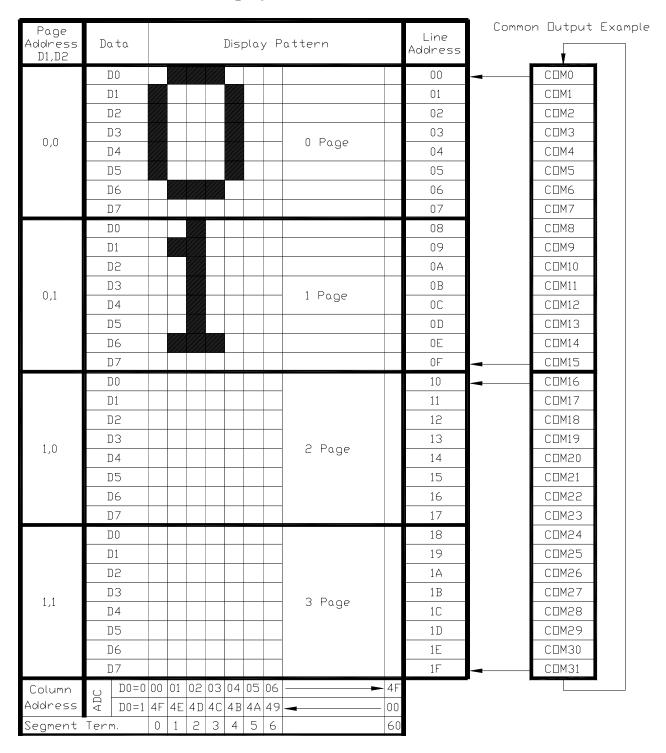
Paramet	Parameter		Min	Max	Condition	Unit
Address set u	p time	t _{Aw6}	40			ns
Address hold	d time	t _{AH6}	20			ns
System cycle	e time	t _{CYC6}	2000			ns
E mulaa width	Read	4	200			ns
E pulse width	Write	t _{Ew6}	160			ns
Data set up	time	t _{DS6}	160			ns
Data hold t	Data hold time		20			ns
Access tin	Access time			180	C =100pE	ns
Output disabl	le time	t _{OH6}	20	180	$C_L=100pF$	ns

^{*}Input signal rise time and fall time are less than 15ns





10. Reflector of Screen and Display RAM





11. DISPLAY CONTROL INSTRUCTION

Instruction	A0	R/W	D7	D6	D5	D4	D3	D2	D1	D 0	Descri	ption	
Display on/off	0	0	1	0	1	0	1	1	1	0/1	Whole disp 1:on	olay on/off 0:off	
Display start line	0	0	1	1	0			play s ess(1			Determine the correspond to	- •	
Page address set	0	0	1	0	1	1	1	0		nge ~3)	Set the page o	- •	
C0lumn address set	0	0	0		Co	lumn	addre	ss(0~	79)		Set the colum display da		
Status read	1	0	B U S Y	A D C	O N / O F F	R E S E T	0	0	0	0	BUSY 0:ready 1:work ADC 0:counterclockw 1:clockwise out ON/OFF 0:disp on 1:disp RESET 0:normal 1:reset	vise out off	
Write display data	0	1	Writ	Write data						Access the predetermine			
Read display data	1	1	Read data From display the				d address of the display RAM						
ADC select	0	0	1	0	1	0	0	0	0	0/1	Determine the n the displa 0: clockwise ou 1: counterclock	ay RAM itput	
Static drive on/off	0	0	1	0	1	0	0	1	0	0/1	Select the dyn driv 1: static driving 0: dynamic driv	amic or static ing	
Duty ratio select	0	0	1	0	1	0	1	0	0	0/1	Select the duty r 0: 1/16 1: 1-		
Read Modify write	0	0	1	1	1	0	0	0	0	0	Increment the cregister when change who	writing but no	
END	0	0	1	1	1	0	1	1	1	0	Release from th	Release from the Read Modify Write mode	
Reset	0	0	1	1	1	0	0	0	1	0	Set the displeregister to 1st lin Register	e, page add.	
Power save (dual command)	0 0	0	1 1	0 0	1 1	0 0	1 0	1 1	1 0	0 1	Set the power selecting displa drivir	y off and static	



Instruction Description

(1) Display On/Off

This is instruction executes whole display On/Off no relation with the data in the Display Data RAM and internal conditions.

 A0
 R/W
 D7
 D6
 D5
 D4
 D3
 D2
 D1
 D0

 code
 0
 0
 1
 0
 1
 0
 1
 1
 D

D 0:Dislpay On;

1:Display Off

when the static driving mode is selected (static drive On) in display Off status, the internal circuits put on the power save mode.

(2) Display Start Line

A0

R/W

D7

D6

D5

This instruction set the line address. The selected line in the Display Data RAM correspond to the COM0 Which display at the of LCD panel.

The display area is set automatically from the selected line to the line which increased the number of duty ratio.

Therefore, the smooth scroll for vertical direction by changing the start line address one by one or page switching are available by this instruction.

D4

code

110 10	• • •	. 20			20			•	
0	0	1	1	0	A4	A3	A2	A1	A0
A4	A3	A2	A1	A0		Li	ine Addres	SS	

D3

D2

D1

D0

A4	A3	A2	A1	A0	Line Address
0	0	0	0	0	0
				1	1
1	1	1	1	0	1E
1	1	1	1	1	1F

(3) Page Address Set

When MPU access the display Data RAM, the page address corresponded to the row address must be selected.

The access in the display Data RAM is available by setting the page and column address.

The display is on change when the page address is changed.

Code

_	A0	R/W I	D7 D6	5 D5	D4	D3	D2	D1 D	00	
	0	0	1	0	1	1	1	0	A1	A0

A1	A0	Page
0	0	0
0	1	1
1	0	2
1	1	3

(4) Column Address Set

The instruction set the column address in the Display Data RAM.

When the MPU access the Display Data RAM continuously ,the column address increase automatically, therefore, the MPU can access the data only without address setting .



The increment of the column address is stopped by the address of 50H automatically ,but the page address is no change even if the column address increase to 50H and stop.

code

A0	R/W	D7 D6	D5	D4	D3	D2	D1 D	00	
0	0	0	A6	A5	A4	A3	A2	A1	A0

A6	A5	A4	A3	A2	A1	A0	Column Add
0	0	0	0	0	0	0	0
0	0	0	0	0	0	1	1
1	0	0	1	1	1	0	4E
1	0	0	1	1	1	1	4F

(5) Status Read

This instruction read out the internal status.

	A0	R/W	D7 D6	D5	D4	D3	D2	D1 D0)		
code	0	1	BUSY	ADC	ON/OFF	RESET	0	0	0	0	

BUSY:BUSY=1 indicate the operating or the Reset cycle.

The instruction can be input after the BUSY status change to

ADC: Indicate the output correspondence of column (segment)address and segment driver.

0:Counter clockwise Output (Inverse)

Column Address 79-n Segment Driver n
1: Clockwise Output (Normal)
Column Address n Segment Driver n

NO/OFF: Indicate the whole display On/Off status.

0:Whole Display n

1:While Display ff

(Note) The data =On and =Off of Display On/Off status read out is inverted with the Display On/Off instruction data of =On and =Off

RESET: Indicate the initialization period by reset instruction.

(6) Write Display Data

This instruction write the 8-bit data on the data bus into the Display RAM.

The column(segment) address increase automatically when writing ,therefore,

The MPU can write the 8-bit data into the Display Data RAM without address setting.

A0 R/W D7 D6 D5 D4 D3 D2 D1 D0
61SEG 1 0 Write Data

(7) Read Display Data

This instruction read out the 8-bit data from Display Data RAM which addressed by the column and page address. In case of the Read Modify Write Mode is off, the column address increase automatically after each read out, therefore, the MPU can read out the 8-dit data from the Display Data RAM continuously without address setting.

	A0	R/V		D7	D6	D5	D4	D3	D2	D1	D0	
Code		1	1	R	ead Data							



(8) ADC Select

This instruction set the correspondence of column address in the Display Data RAM and segment driver out. Therefore, the order of segment output can be changed by the software, and no restriction of the LSI placement against the LCD panel.

Code

AO R	/W]	D7	D6	D5	D4	D3	D2	D1 D	00	
0	0	1	0	1		0	0	0	0	D

D D 0: Clockwise Output (Inverse)
1: Counter Clockwise Output (Normal)

(9) Static Drive On/Off

This instruction executes the all common output terns on and whole display on obligatory.

D₀ A0 R/W D7 **D6** D₅ D4 D3 D2D1 Code 0 0 0 1 0 1 0 1 0 D

D 0: Static Drive Off

(Normal Operation)

1: Static Drive On

(Whole Display Turns On)

When the Display Off mode is selected (Display Off) in Static On status. the internal circuits put on the power save mode.

(10) Duty Select

This instruction set the LCD driving duty ratio.

A0 R/W D7 **D6 D5** D4 D3 D2D1D00 0 1 0 1 0 1 0 0 D

Code

D 0: 1/16 duty 1: 1/32 duty

(11) Read Modify Write

After this instruction is executed, the column address increase—automatically when Display Data Write Instruction execution, but the address is not changed when the Display Data Read Instruction execution.

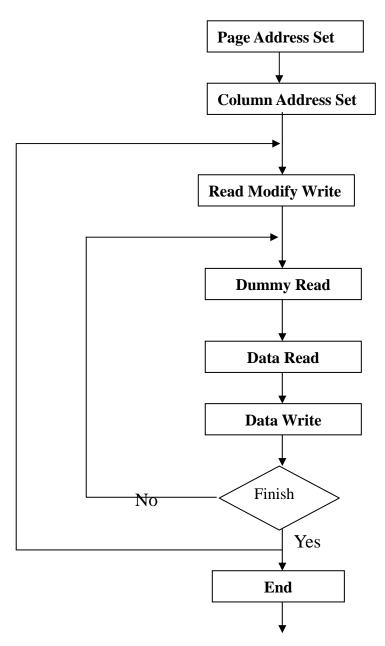
This status continues during End instruction .When the End instruction is entered the column address back to the address where Read Modify Write instruction entering. By this function, the load of MPU for example cyclic data writing operation like as cursor blink etc., can be reduced.

A0 R/W D7 **D6 D5** D4 D3 D1 D0D2Code 0 0 1 1 0 1 0 0 0 0

(Note)During the Read Modify Write mode, any instruction except Column Address Set can be executed.



(12) Sequence of cursor display



(13) End

This instruction release the Read Modify Write mode and the column address back to the address where the Read Modify write mode setting.

	A0	R/W	D7	D6	D5	D4	D3	D2	D1	D0			
Code	0	0	1	1	1	1	0	1	1		1	0	

(14) Reset

This instruction executes the following initialization.

Initialization

- 1) Set the first line the Display Start Line Register.
- 2) Set the page 3 in the page Register.

In this time, there are no influence to the Display Data RAM.

	A0	R/W I	D7 D6	D5	D4	D3	D2	D1 D0		
Code	0	0	1	1	1	0	0	0	1	0



(Note) The initialization when the power terns on can no be executed by Reset instruction.

(15) Power save (Dual Command)

When both of Display Off and static Drive On are executed, the internal put on the power save mode and the current consumption is reduced as same as stand by current.

The internal status in this mode are as follows:

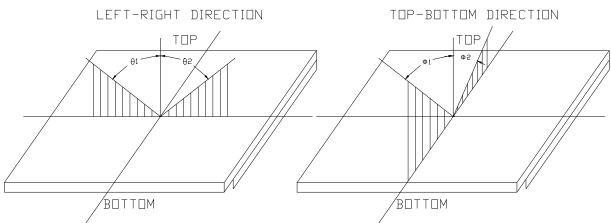
- 1) Stop the LCD driving . Segment and Common drivers output Vdd level.
- 2) Stop the oscillation or inhibit the external clock input.
- 3) Keeping the display data and operating mode.

The power save mode is released by Display on or static drive off instruction.

12. OPTICAL CHARACTERISTICS:

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REF.
Contrast	CR	25°C, Vdd=5V, θ =0, Ø=0		4			(2)
Rise Time	Tr	25°C, Vdd=5V, θ =0, Ø=0		160	240	ms	(3)
Fall Time	Tf	25°C, Vdd=5V, θ =0, Ø=0		100	150	ms	(3)
Viewing Angle	θ 1- θ 2	25℃	70		90	DEC	(1)
	Ø1, Ø2	23 C	-40		40	DEG	(1)

(1)Definition of viewing Angle:



(2) Definition of Contrast Ratio:

Contrast Ratio= Brightness of non-selected condition
Brightness of selected condition

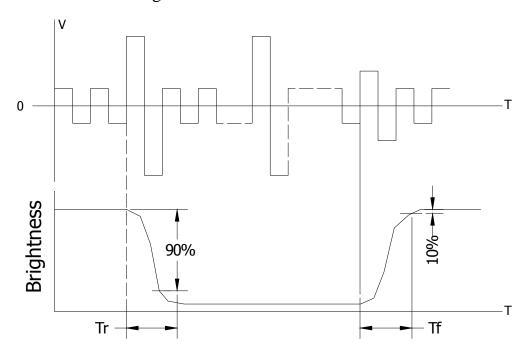
Test condition: standard A light source

(3)Response Time:

Response time is measured as the shortest period of possible between the change in

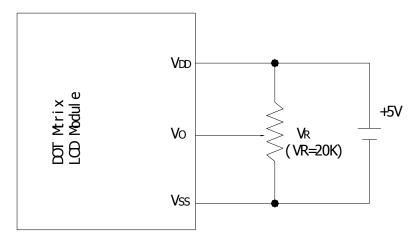


state of an LCD segments as demonstrated below:



13.POWER SUPPLY SCHEMATICS

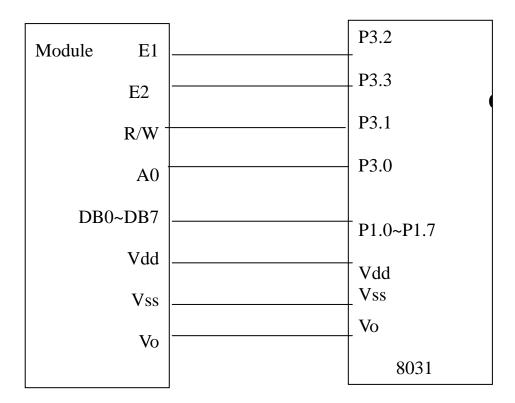
For Single Source



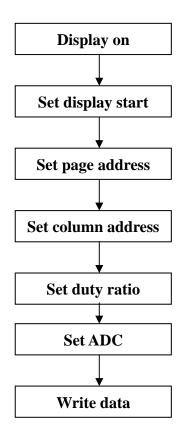


11. APPLICATION EXAMPLE

(1) Application Circuit



(2) Application Flowchart





13. PRECAUTION FOR USING LCM

1. LIQUID CRYSTAL DISPLAY (LCD)

LCD is made up of glass, organic sealant, organic fluid, and polymer based polarizers. The following precautions should be taken when handing,

- (1). Keep the temperature within range of use and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel off or bubble.
- (2). Do not contact the exposed polarizers with anything harder than an HB pencil lead. To clean dust off the display surface. Wipe gently with cotton. Chamois or other soft material soaked in petroleum benzine.
- (3). Wipe off saliva or water drops immediately. Contact with water over a long period of time may cause polarizer deformation or color fading, while an active LCD with water condensation on its surface will cause corrosion of ITO electrodes.
- (4). Glass can be easily chipped or cracked from rough handing. especially at corners and edges.
- (5). Do not drive LCD with DC voltage.

2. Liquid Crystal Display Modules

2.1 Mechanical Considerations

LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.

- (1). Do not tamper in any way with the tabs on the tabs on the metal frame.
- (2). Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
- (3). Do not touch the elastomer connector, especially insert an backlight panel (for example, EL).
- (4). When mounting a LCM make sure that the PCB is not under any tress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
- (5). Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pie l's.

2.2. Static Electricity

LCM contains CMOS LSI's and the same precaution for such devices should apply, namely

- (1). The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.
- (2). The modules should be kept in antistatic bags or other containers resistant to static for storage.
- (3). Only properly grounded soldering irons should be used.
- (4). If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.
- (5). The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.
- (6). Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

2.3. Soldering

- (1). Solder only to the I/O terminals.
- (2). Use only soldering irons with proper grounding and no leakage.
- (3). Soldering temperature: 280 $^{\circ}\text{C} \pm 10^{\circ}\text{C}$
- (4). Soldering time: 3 to 4 sec.
- (5). Use eutectic solder with resin flux fill.
- (6). If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed after wards.

2.4. Operation

- (1). The viewing angle can be adjusted by varying the LCD driving voltage V0.
- (2). Driving voltage should be kept within specified range; excess voltage shortens display life.
- (3). Response time increases with decrease in temperature.



- (4). Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
- (5). Mechanical disturbance during operation (such as pressing on the viewing area) nay cause the segments to appear "fractured".

2.5. Storage

If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. Never swallow the fluid. The toxicity is extremely low but caution should be exercised at all the time.

2.6. Limited Warranty

Unless otherwise agreed between SANTECH and customer, SANTECH will repair or repair any of its LCD and LCM, which is found to be defective electrically and visually when inspected in accordance with SANTECH acceptance standards, for a period on one year from data of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of SANTECH is limited to repair and/or replacement on the terms set forth above. SANTECH will not responsible for any subsequent or consequential events.